

Implementation

- Read instruction at PC
 - *Send PC to memory*
- Decode, determine format, determine operation
 - *Pull specific bits, route through logic gates*
- Read values from source registers
 - *Send bits from instruction code to register file*

Implementation

- Perform operation (add)
 - *Arithmetic and Logic Unit (ALU)*
 - *Send register values to ALU*
 - *Send bits coding for specific operation to ALU*
- Write result into destination register
 - *Route ALU signals and RD bits to register file*
- Increment PC