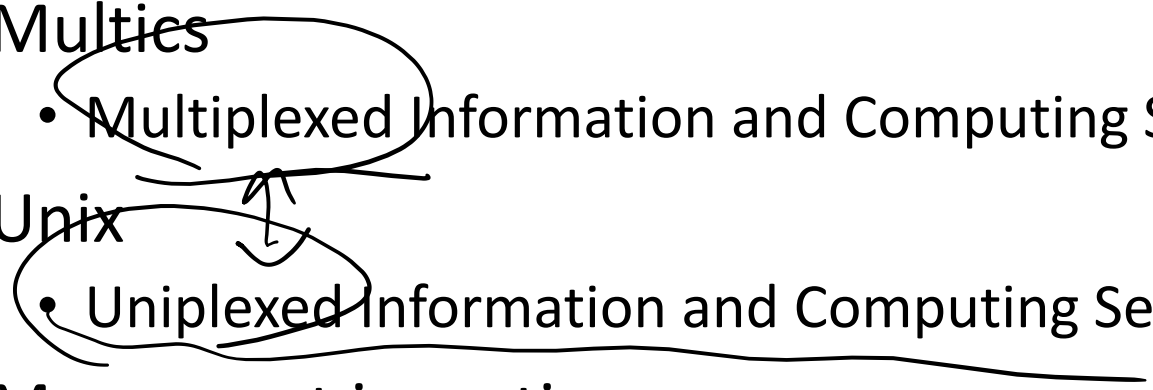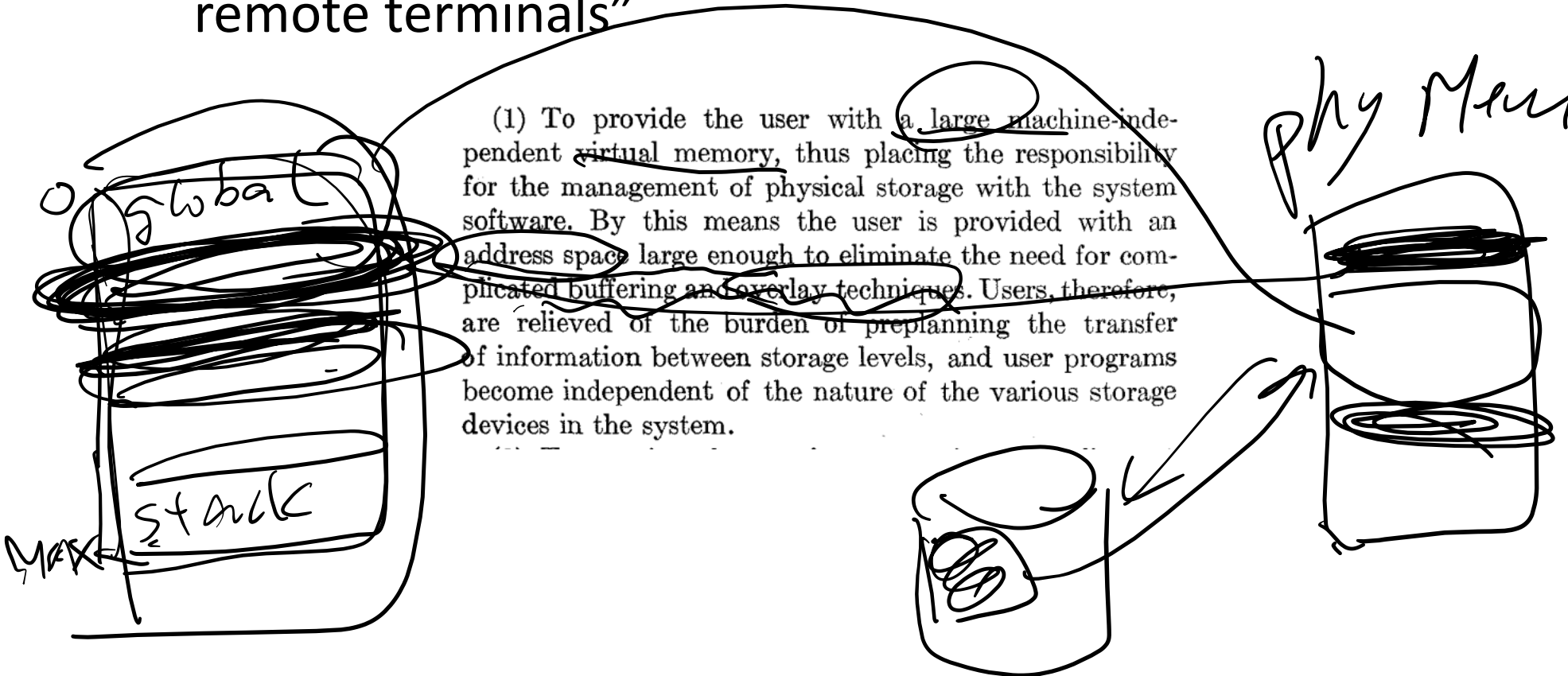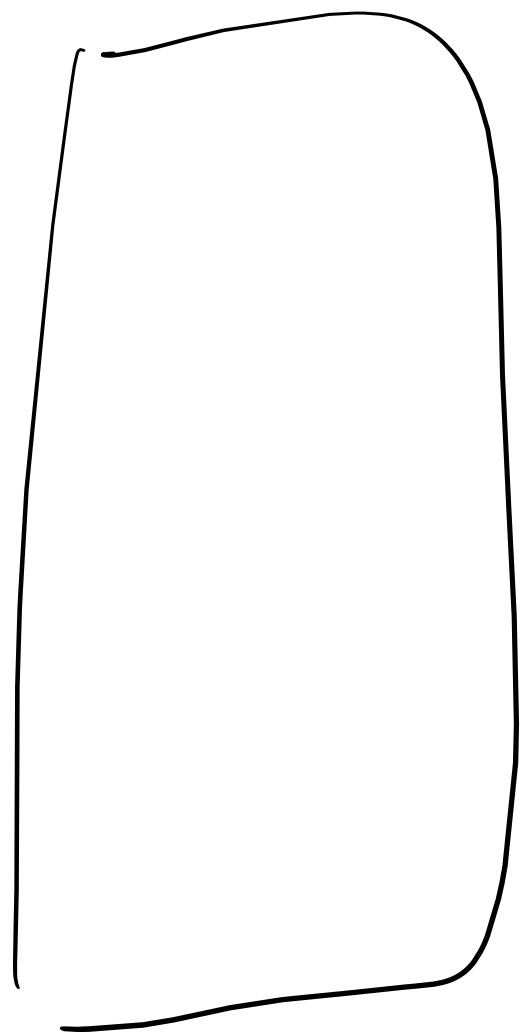# Multics

Background

- Multics
  - Multiplexed Information and Computing Service
- Unix
  - Uniplexed Information and Computing Service
- Many great inventions
  - Virtual memory
  - Security protection
  - File system

Goals

working set

- "so the system would effectively serve the computing needs of a large community of users with diverse interests, operating principally from remote terminals"

phy Mem

globa

Max

stack

(1) To provide the user with a large machine-independent virtual memory, thus placing the responsibility for the management of physical storage with the system software. By this means the user is provided with an address space large enough to eliminate the need for complicated buffering and overlay techniques. Users, therefore, are relieved of the burden of preplanning the transfer of information between storage levels, and user programs become independent of the nature of the various storage devices in the system.

Goal

(2) To permit a degree of programming generality not previously practical. This includes the ability of one procedure to use another procedure knowing only its name, and without knowledge of its requirements for storage, or the additional procedures upon which it may in turn call.

(3) To permit sharing of procedures and data among users subject only to proper authorization. Sharing of

(1) To provide the user with a large machine-independent virtual memory, thus placing the responsibility for the management of physical storage with the system software. By this means the user is provided with an address space large enough to eliminate the need for complicated buffering and overlay techniques. Users, therefore, are relieved of the burden of preplanning the transfer of information between storage levels, and user programs become independent of the nature of the various storage devices in the system.

Outline

- Virtual memory background
- Virtual memory in Multics
- Data sharing
- Code sharing

Backround

Virtual address, physical address, address space
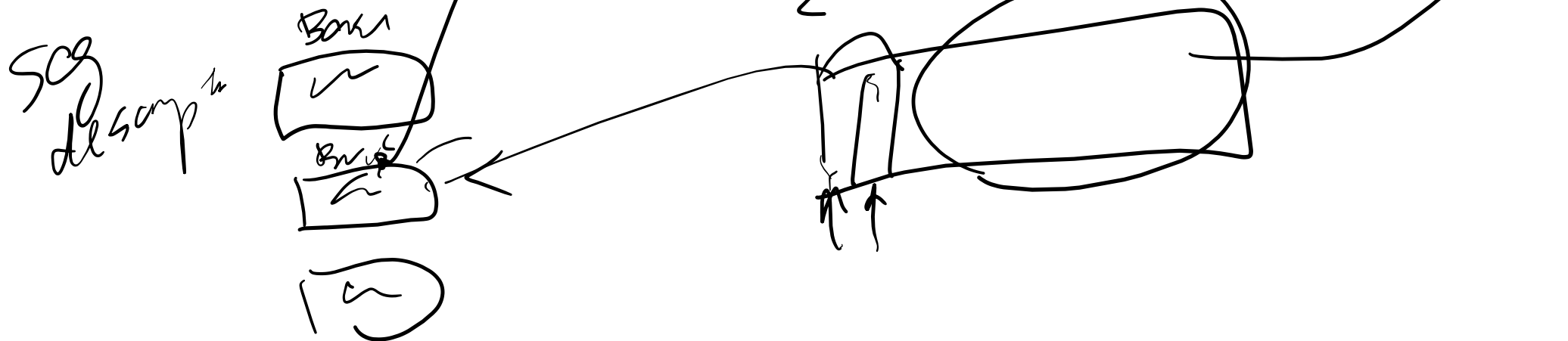
Benefits of virtual memory

- Programming becomes easy

- More portable across machines

- Easier physical memory management
  - No need of contiguous allocation
  - Large virtual address space on small physical memory

- Benefit for MULTICS particularly
  - ?

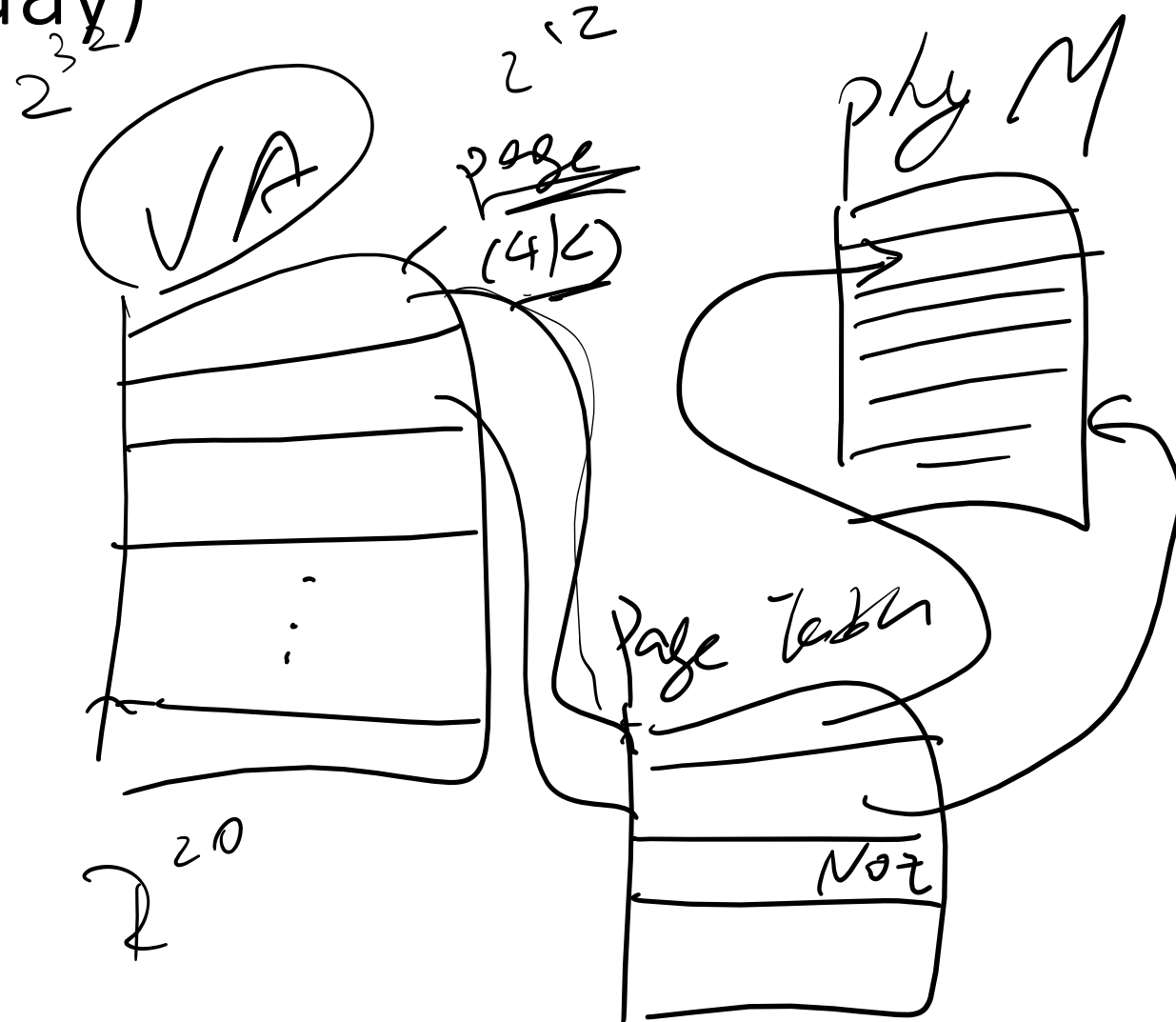# Segmentation (today)

- How many segments?

- How to translate VA to PA?

Paging (today)
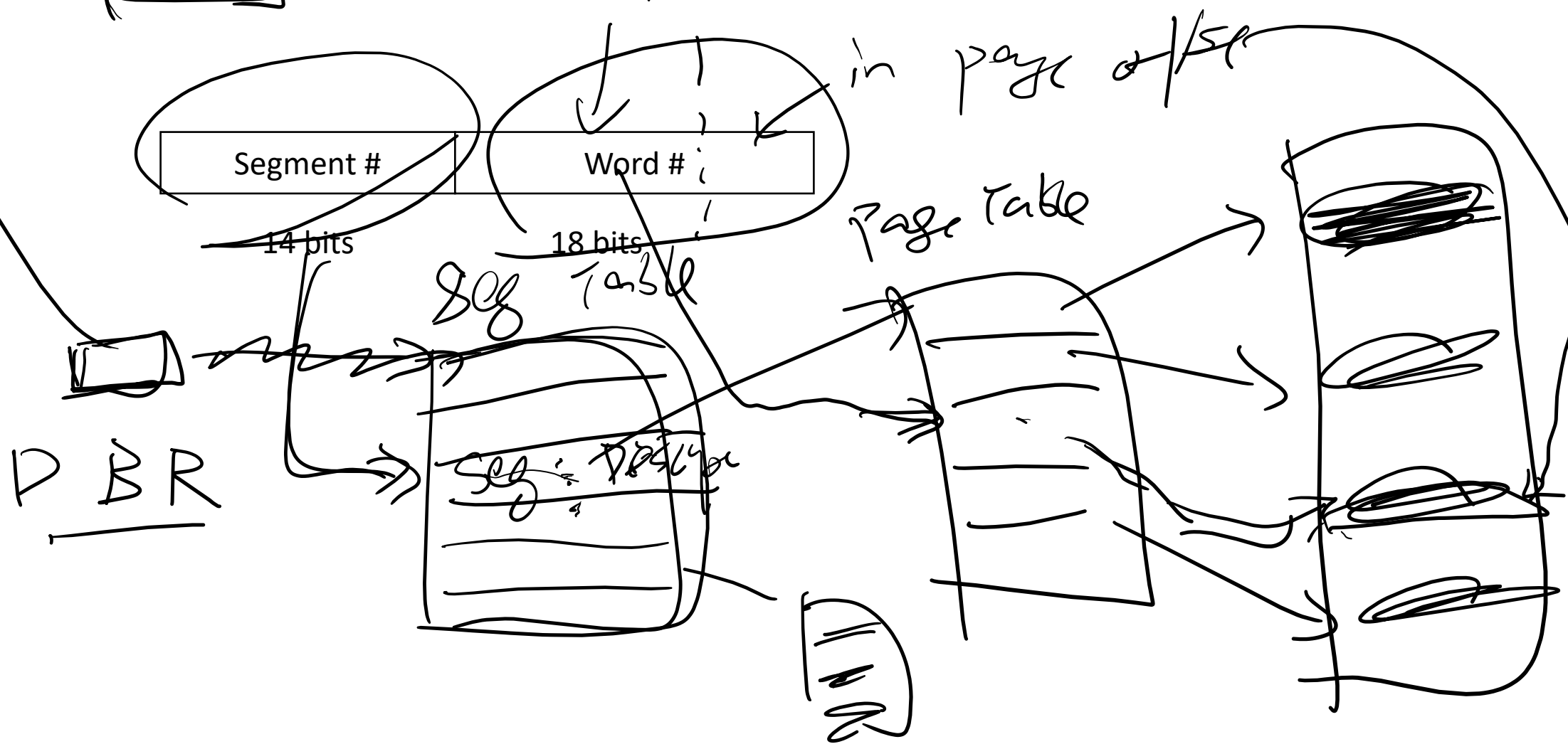
- Page frame
- Page
- Page table

Segment vs. Paging

- What is the benefit of paging over segment?

- What is the benefit of segment over paging?

Virtual Memory in Multics

Virtual address

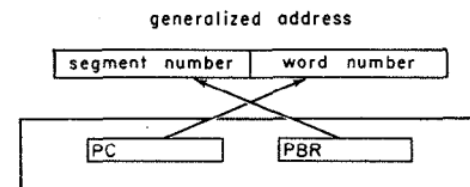- What are the two components of VA in Multics?

ST

page #

in page of/se

Segment #	Word #
14 bits	18 bits

Page Table

Seg Table

Seg : Pray..

P B R

Where does the GA (VA) come from?

generalized address

segment number	word number

| PC | | PBR | |

Fig. 5. Address formation for instruction fetch

generalized ·address

segment number	word number

segment number	word number

base register

segment tag

mode
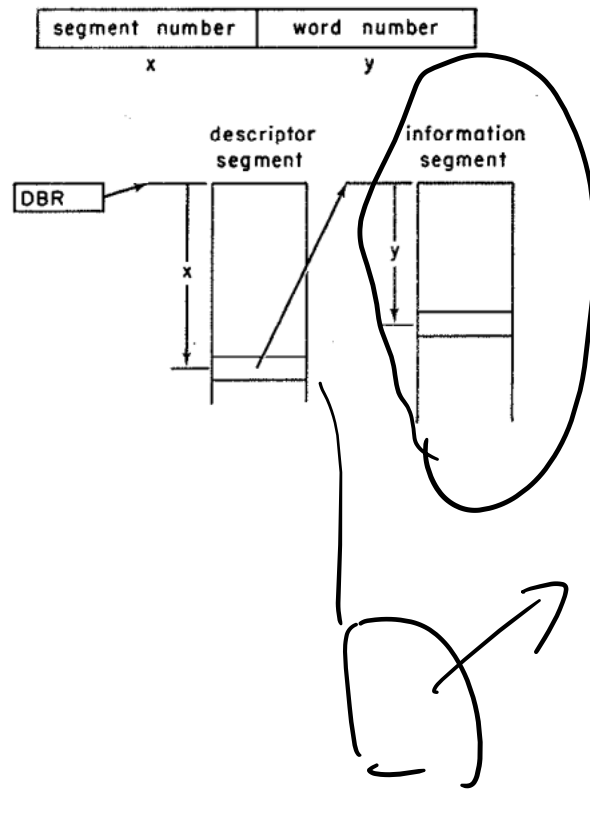
index reg.

	address	OPR	1	
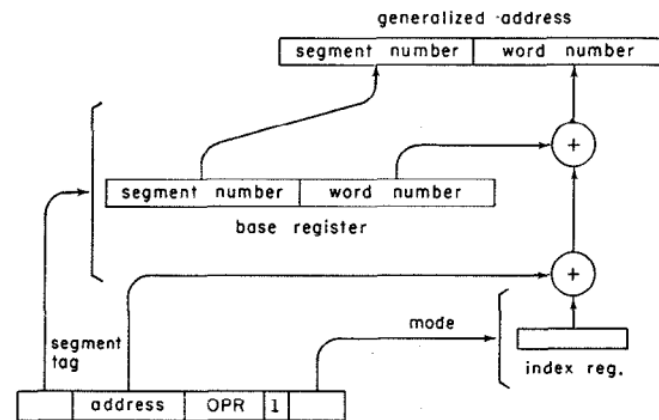
Fig. 6. Address formation for data access

How to translate VA → PA?

- Where is each segment's information stored?

- How does the machine locate that place?

segment number | word number
x | y

descriptor segment

information segment

DBR

x

y

- What else is inside a segment descriptor?

- Does every process have its own descriptor segment?

- What set up the descriptor segment?

- What if the segment is very long?

- What needs to change at context switch?

Data & Code sharing

Requirements / Goals

(1) Procedure segments must be *pure*; that is, their execution must not cause a single word of their content to be modified.

(2) It must be possible for a process to call a routine by its symbolic name without having made prior arrangements for its use.

(3) Segments of procedure must be invariant to the recompilation of other segments.

or

```
int a;
void P::fun(){
    ...
    a = 0;
    tmp = D::x;
    ...
}
```
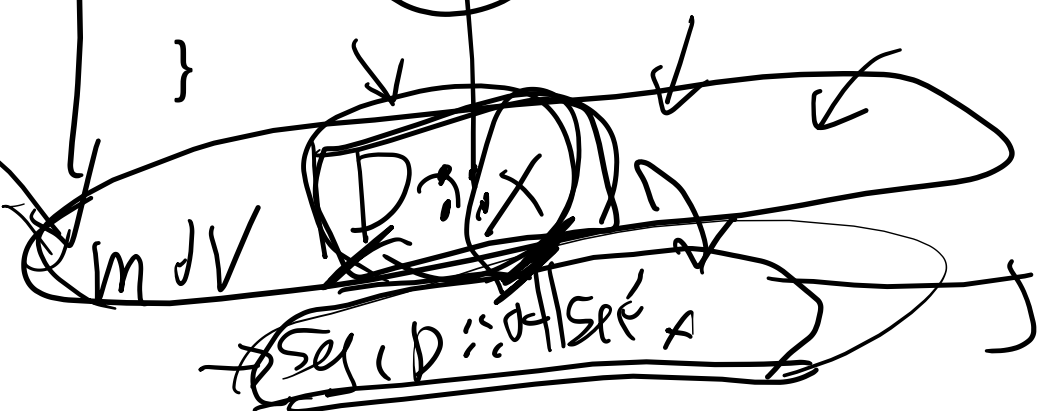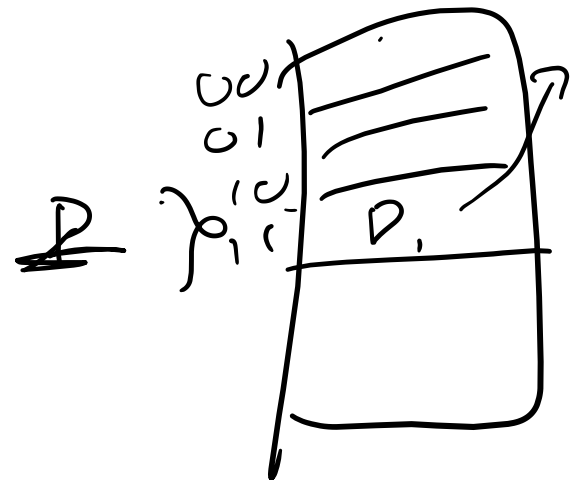
What segment does *a* belong to?

What segment does *tmp* belong to?

What segment does D::x belong to?

Does compiler know the segment # of D?

D::x

m ∂ V

Seg (D::x) Seg x

main ( ) {
if (bar) { E
else
    fun

D

```
int a;
void P::fun(){
    …
    a = 0;
    tmp = D::x;
    …
}
```
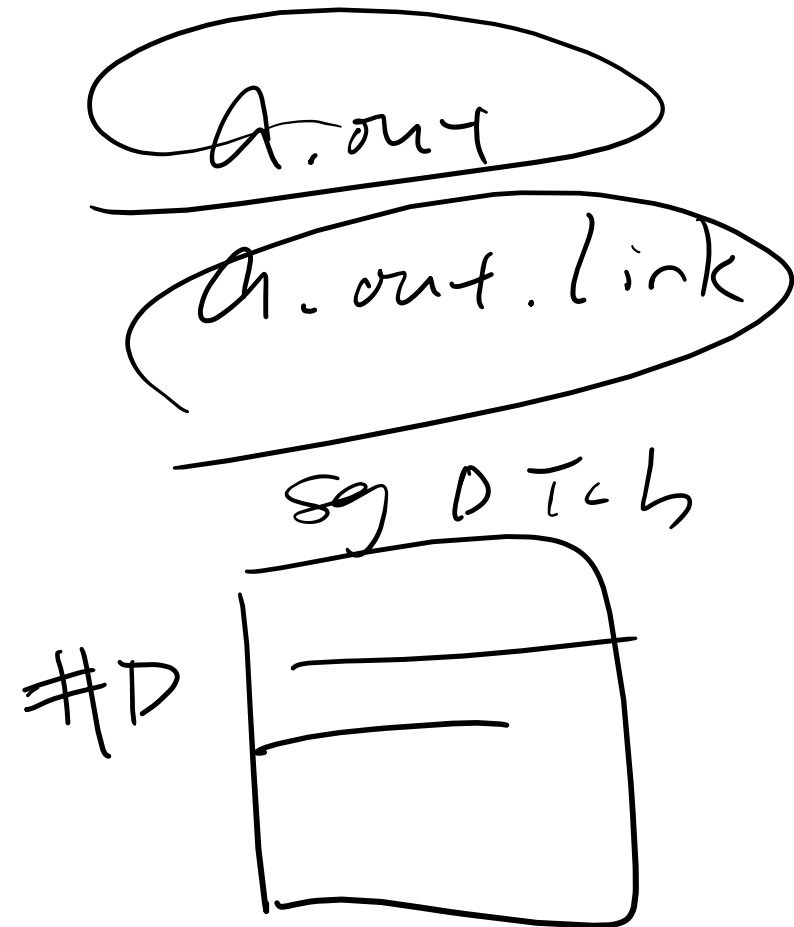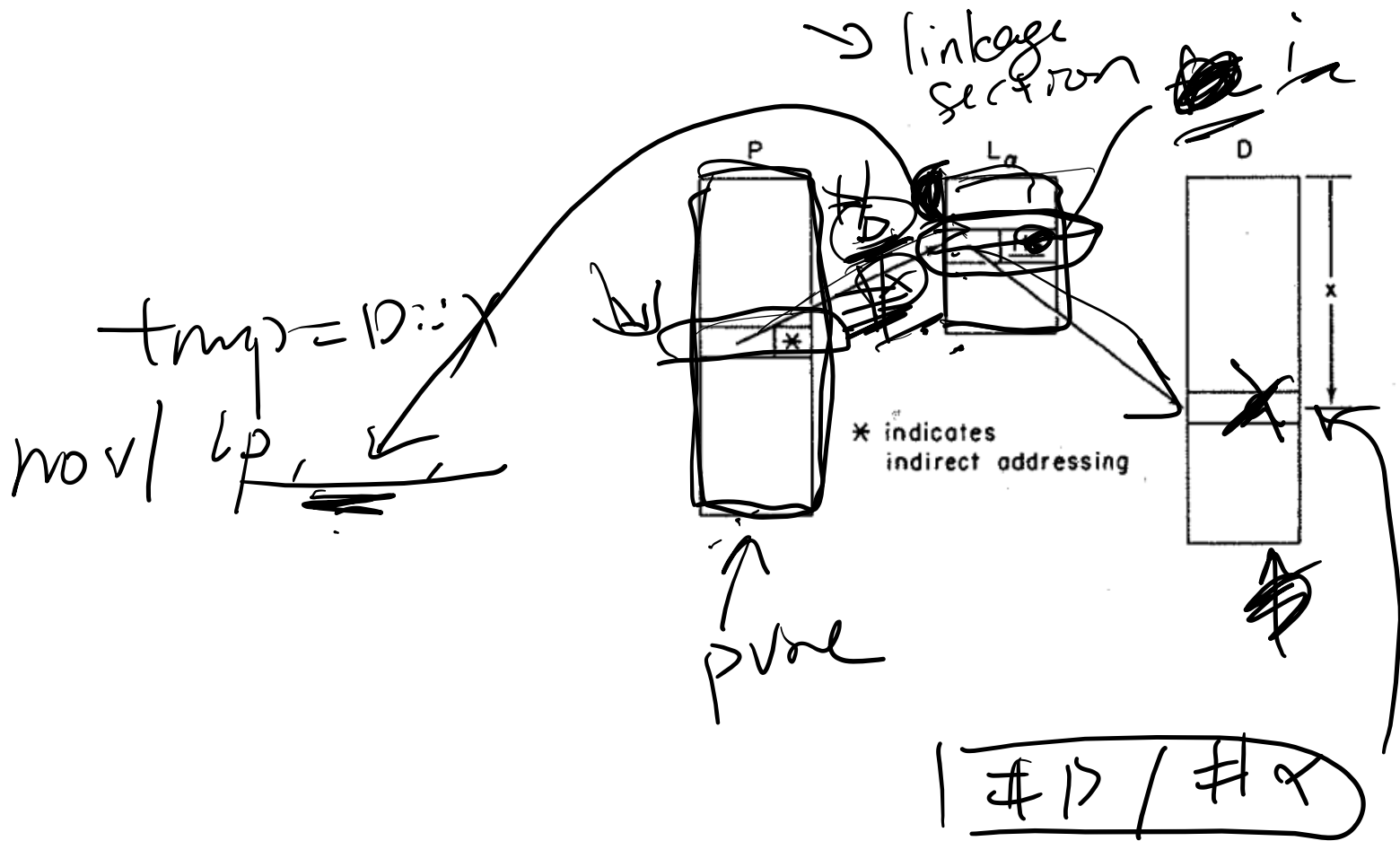
What segment does *a* belong to?

What segment does *tmp* belong to?

What segment does D::x belong to?

Does compiler know the segment # of D?

How is D::x represented in the code?

linkage section

P

L

D

tmp = D::X

movl LP

* indicates indirect addressing

pvar

#D / #α

a.out

a.out.link

sg D Tcb

#D

What are the challenges of code sharing?